

FIG. 1  
 (PRIOR ART)

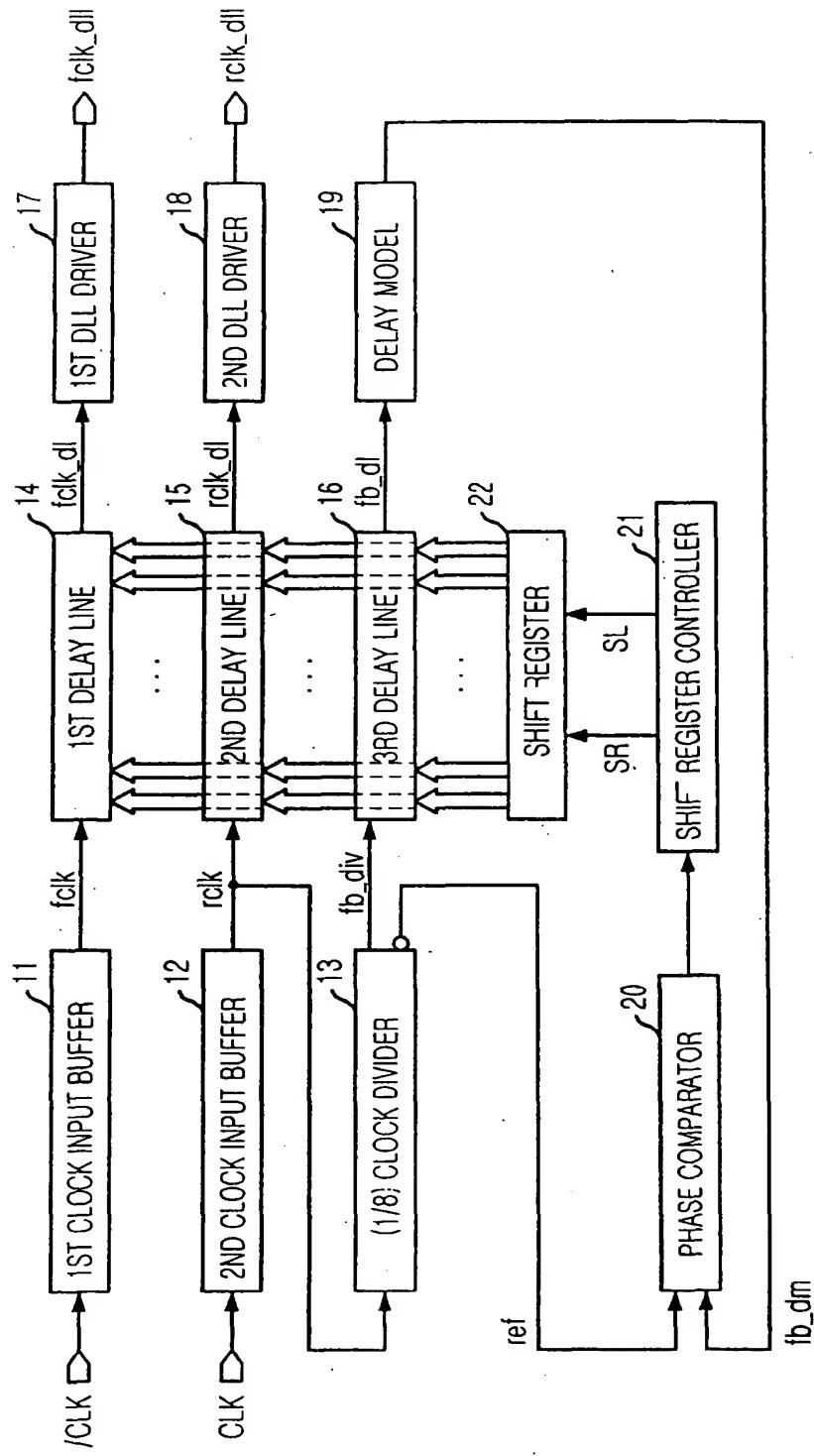


FIG. 2  
(PRIOR ART)

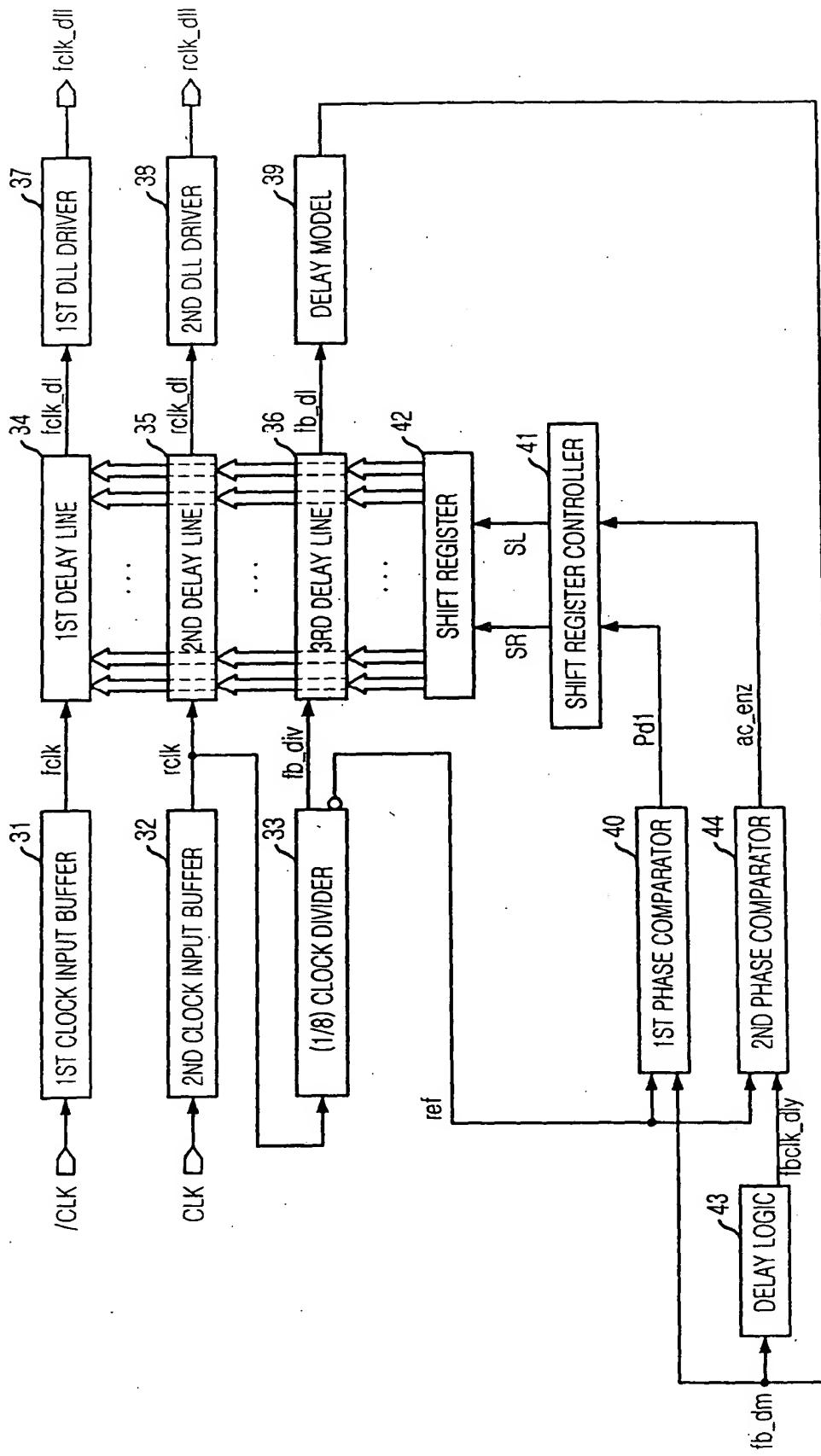


FIG. 3  
 (PRIOR ART)

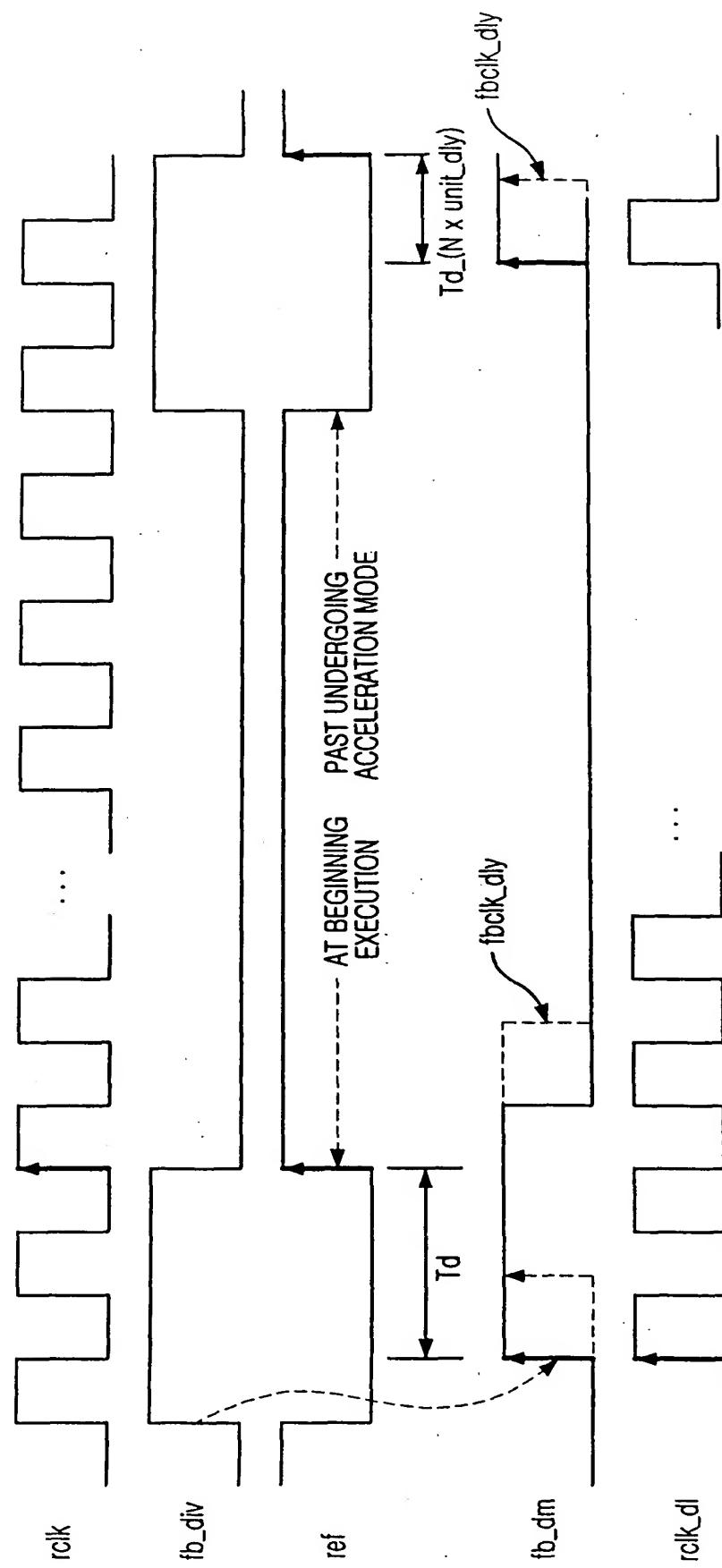


FIG. 4

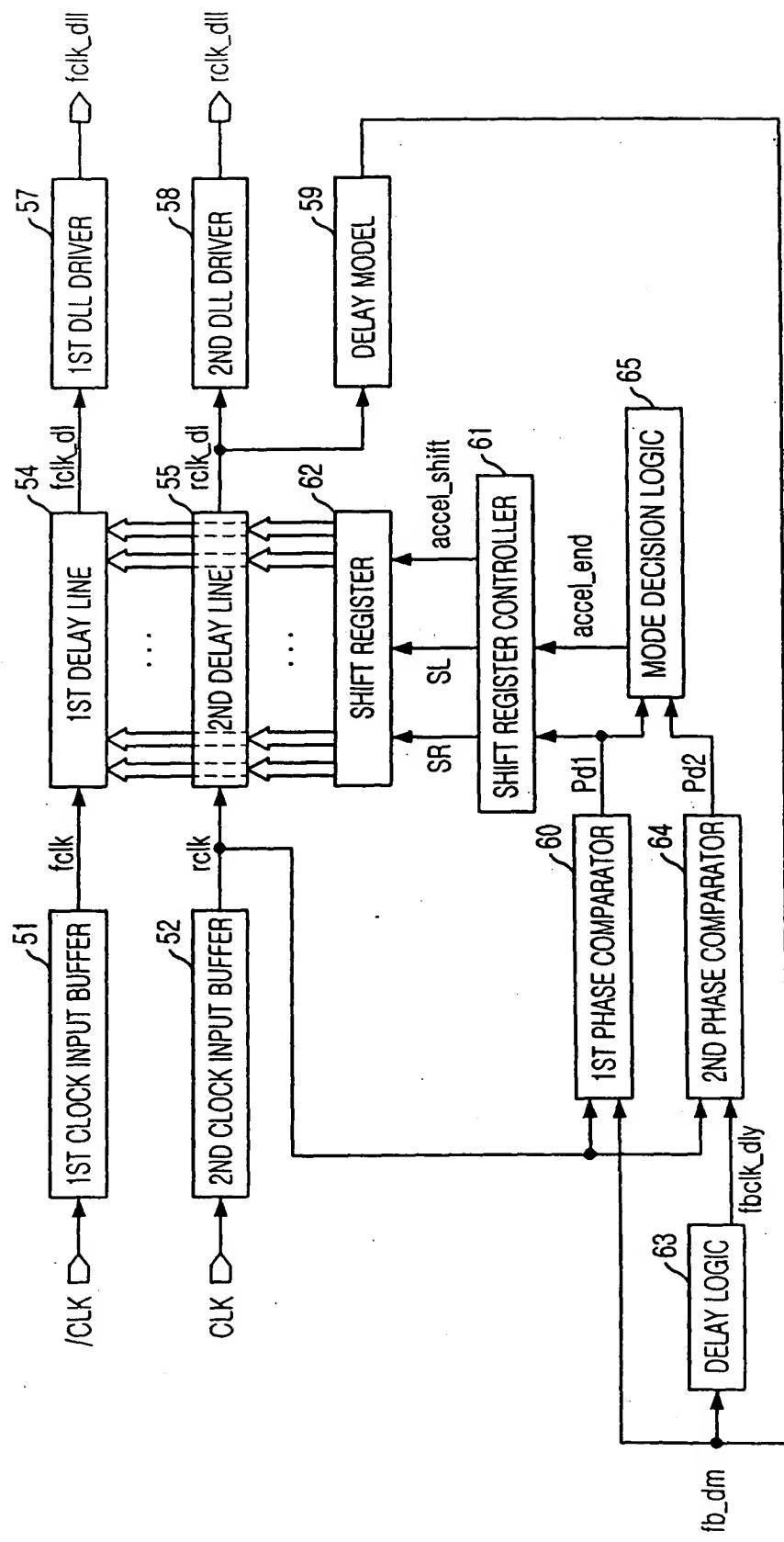
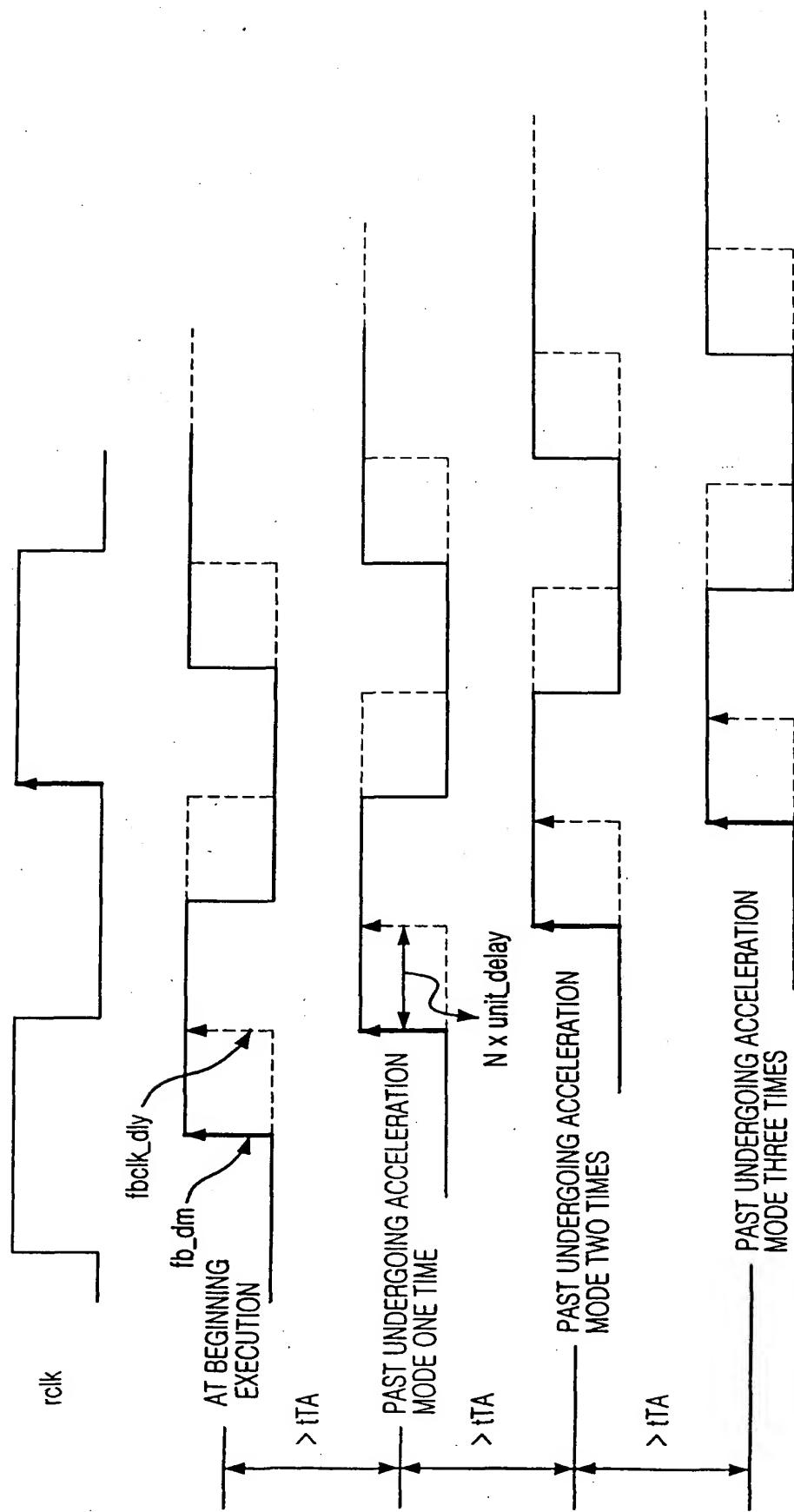
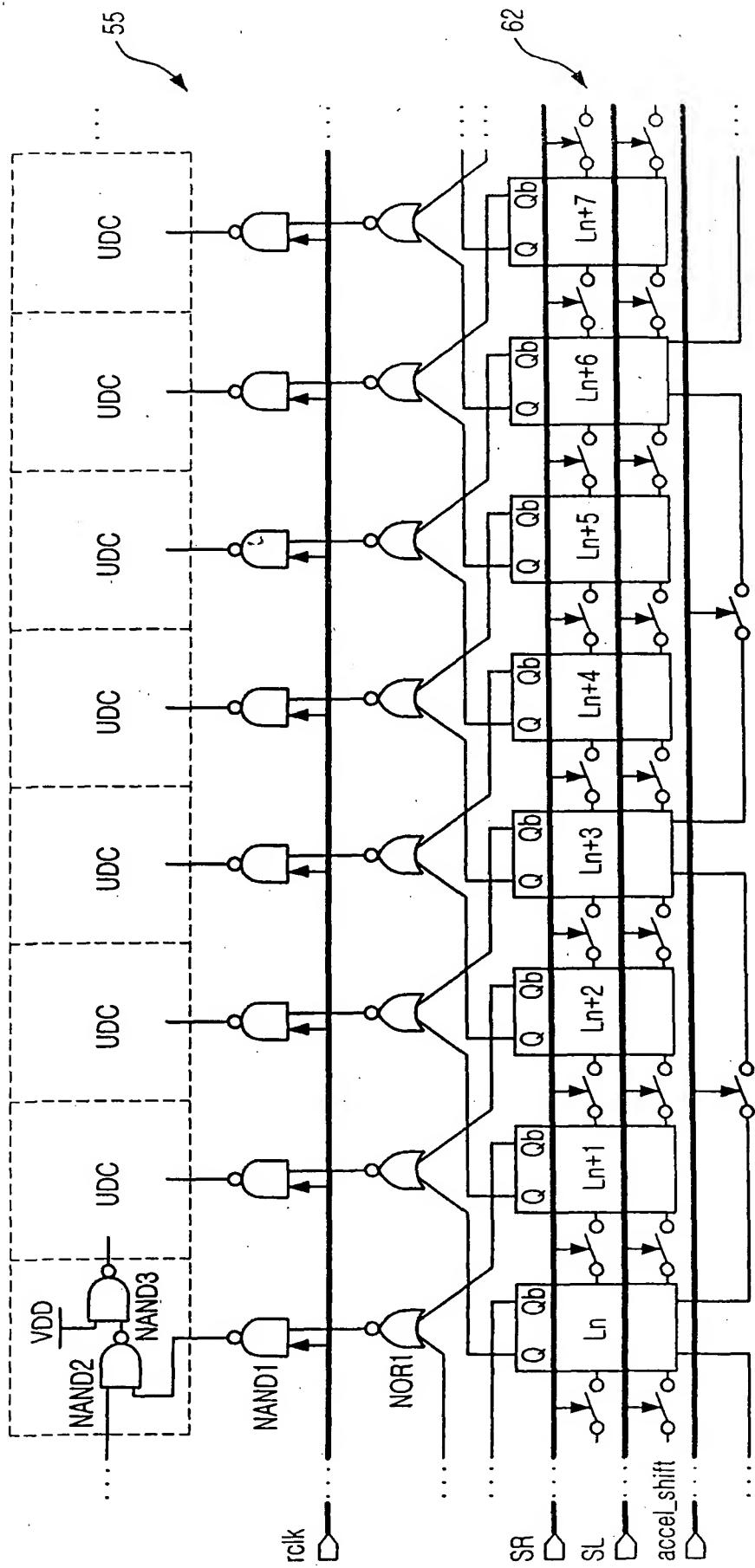


FIG. 5



"Register Controlled Delay Locked Loop  
Having Acceleration Mode"  
Kwak et al. - Appl. No.: NEW  
Atty. Docket: 29926/39503  
Fig. 6 (6 of 10 pages)

FIG. 6



"Register Controlled Delay Locked Loop  
Having Acceleration Mode"  
Kwak et al. - Appl. No.: NEW  
Atty. Docket: 29926/39503  
Fig. 7A (7 of 10 pages)

FIG. 7A

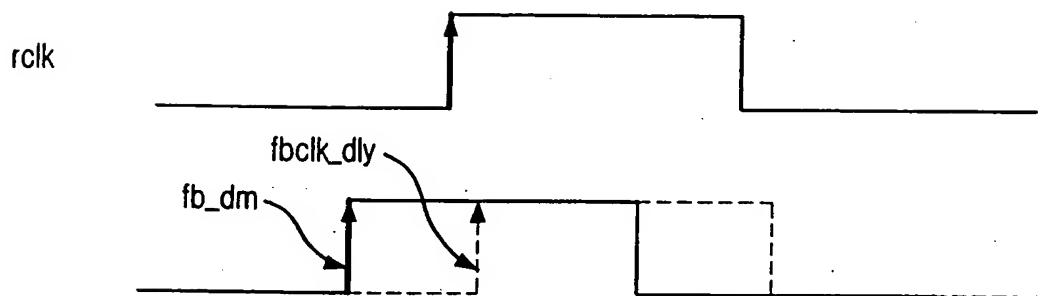
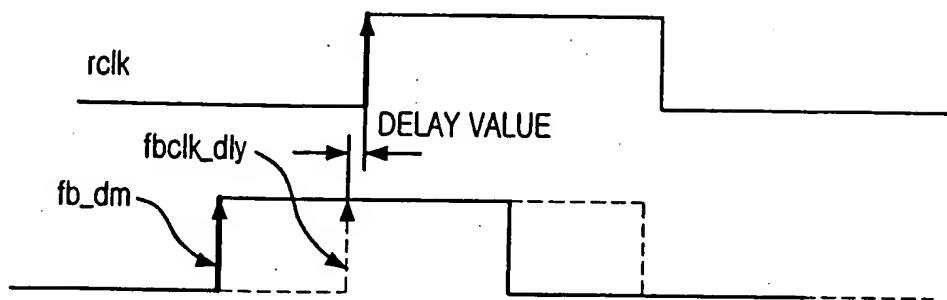


FIG. 7B

PAST UNDERGOING ACCELERATION MODE K TIMES



PAST UNDERGOING ACCELERATION MODE K+1 TIMES

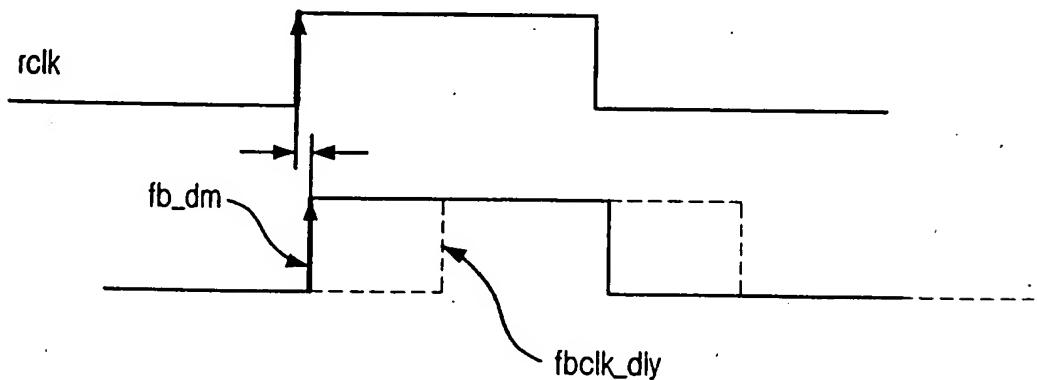


FIG. 8

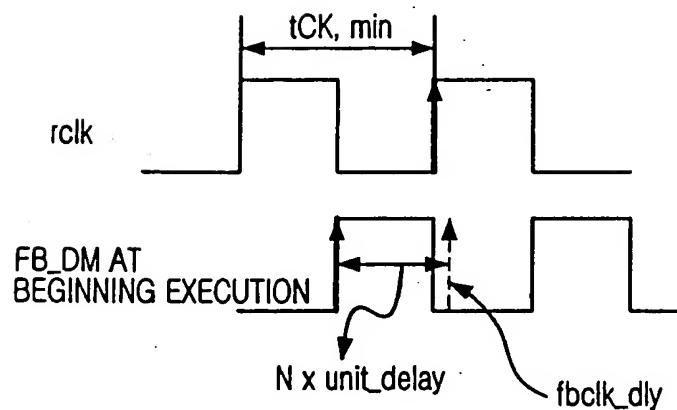


FIG. 9

